

Institute of Electrical Engineering (ETI) **Power Electronic Systems** Kaiserstr. 12 Bldg. 11.10 76131 Karlsruhe, Germany www.eti.kit.edu

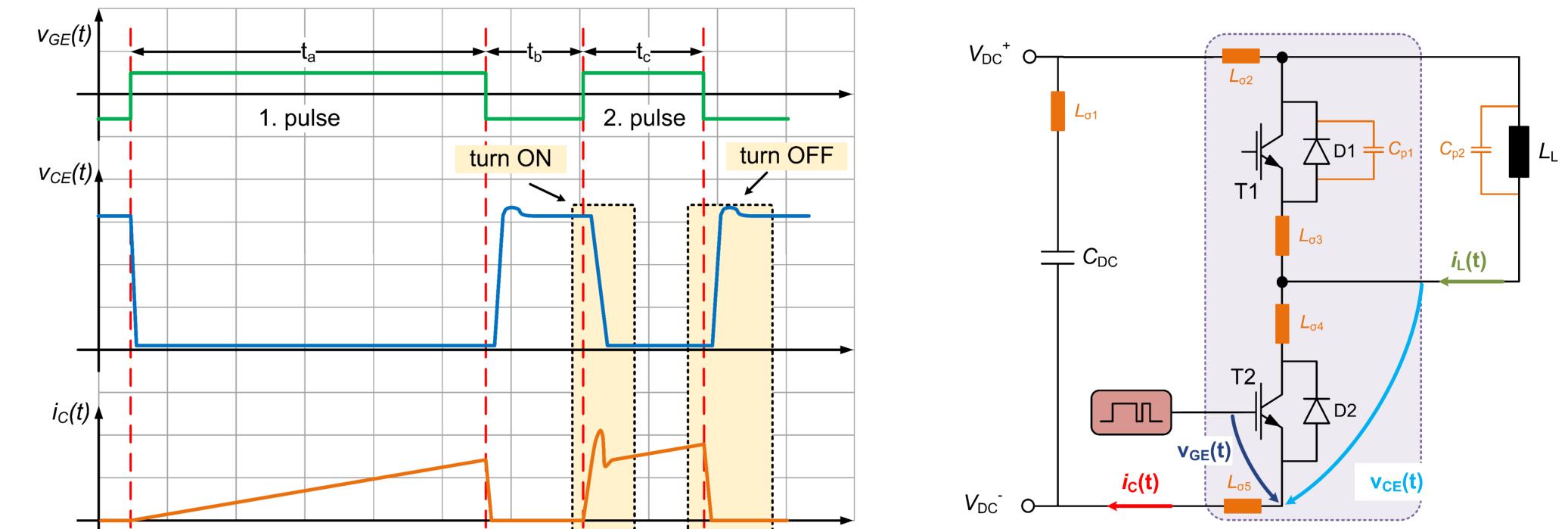


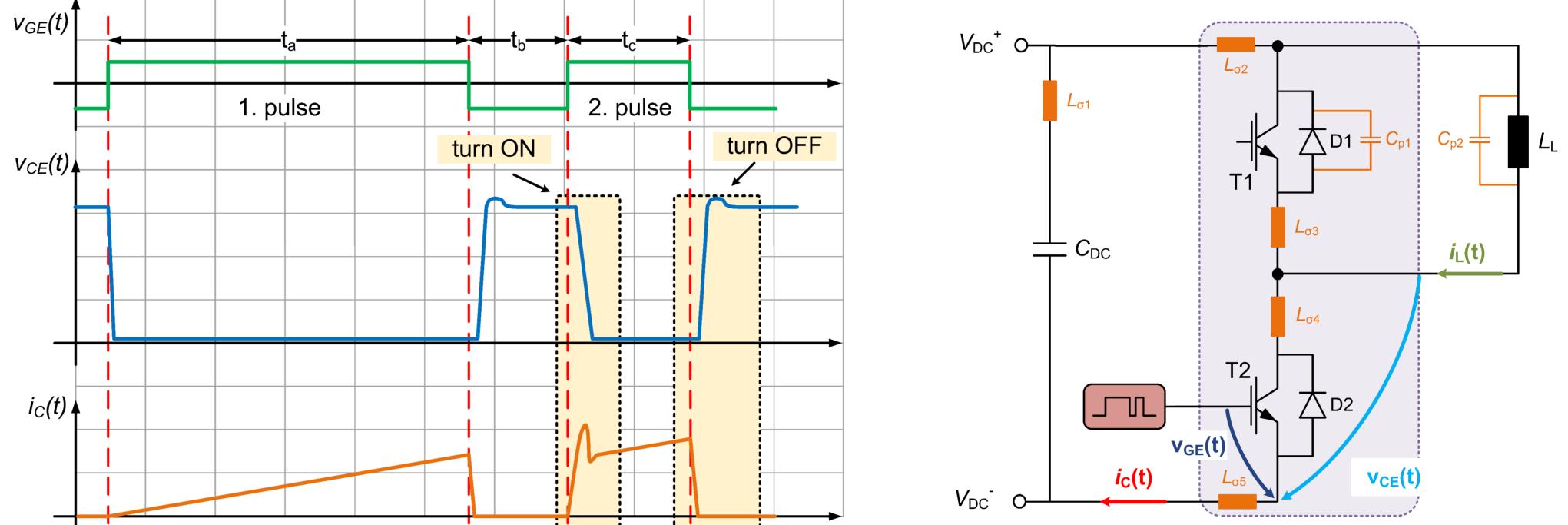
## ETI Double Pulse test bench for measurement and qualification of power semiconductors

M.Sc. Fabian Stamer, M.Sc. Rüdiger Schwendemann, Prof. Dr.-Ing. Marc Hiller contact person: Fabian Stamer - fabian.stamer@kit.edu

**Double Pulse Test Operating Principle** 

Single phase module setup, testing the IGBT T2

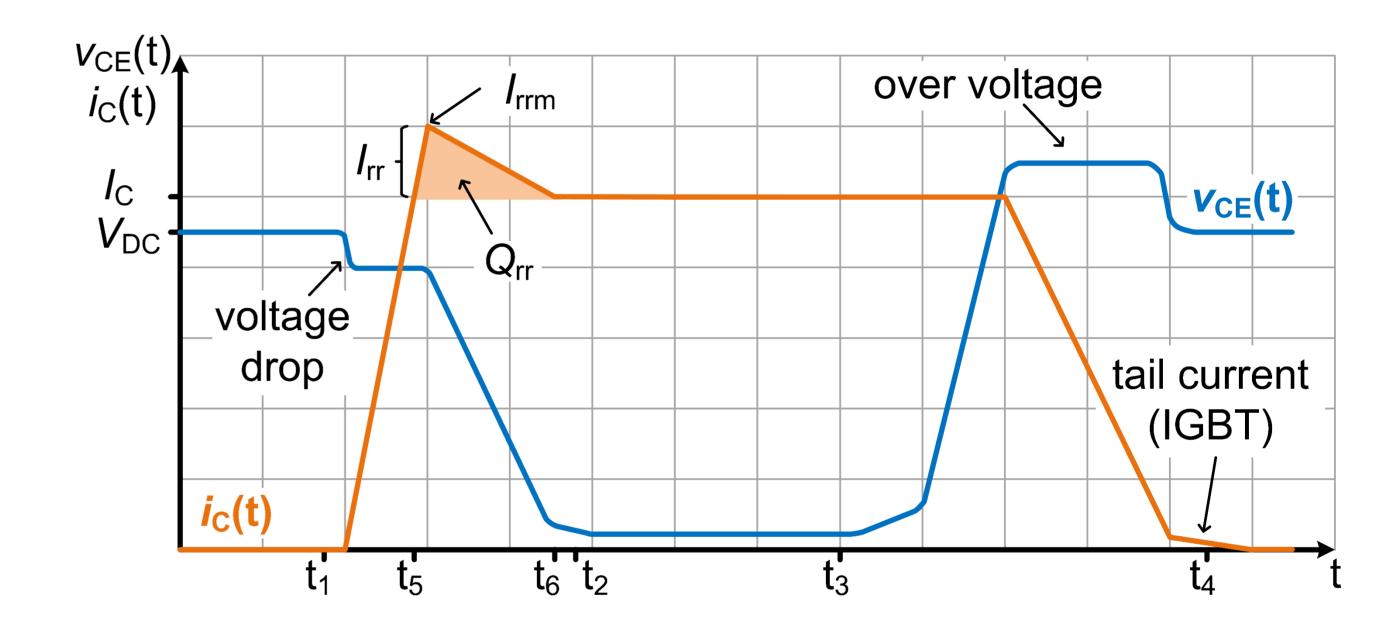




- Characteristic waveforms of the double pulse test
- Control (Gate) Signal green
- Measuring turn ON and OFF behavior at the second pulse
- Measuring at the same time two different operating points

## Loss Calculation and Definition

- Calculation of switching energies
  - $E_{\rm ON} = \int_{t_1}^{t_2} v_{\rm CE}(t) \cdot i_{\rm C}(t) dt$
  - $E_{\text{OFF}} = \int v_{\text{CE}}(t) \cdot i_{\text{C}}(t) dt$
- Used integration limits
  - $t_1: v_{\text{GE}}(t) = 0.1 \cdot V_{\text{GE,on,static}}$  $t_2$ :  $v_{CE}(t) = 0.02 \cdot V_{DC-Link}$  $t_3: v_{\text{GE}}(t) = 0.9 \cdot V_{\text{GE,on,static}}$
- Typical switching characteristics of an IGBT



$$E_{\rm rr} = \int_{t_5}^{t_6} i_{\rm rr}(t) \cdot v_{\rm D}(t) dt$$
$$i_{\rm rr}(t) = i_{\rm C}(t) - I_{\rm C,nominal}$$

test

chamber

DC link

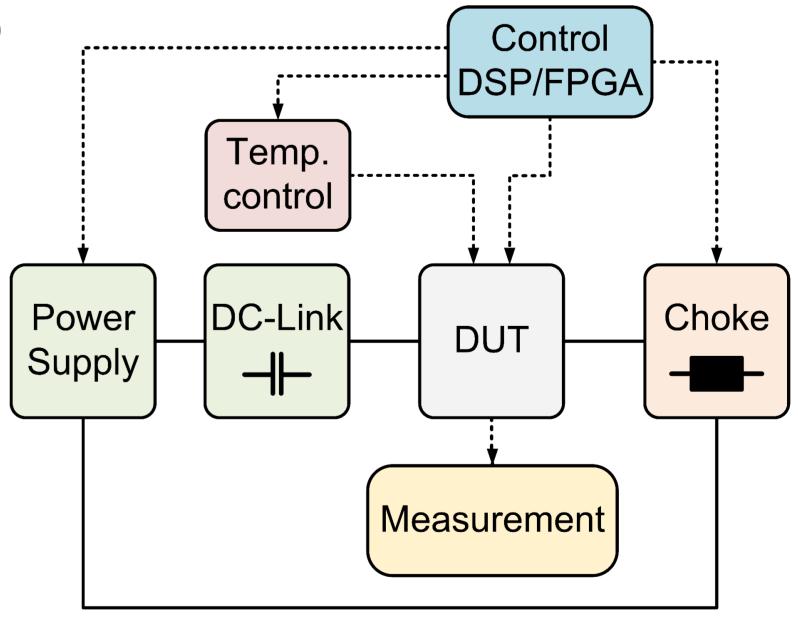
capacitors

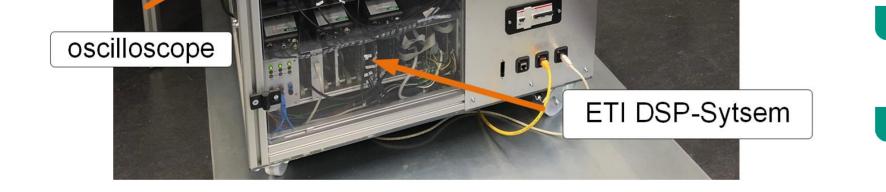
power supply

$$t_4: i_C(t) = 0.02 \cdot I_{C,nominal}$$
$$t_5: i_C(t) = I_{C,nominal}$$
$$t_6: i_D(t) = 0.02 \cdot I_{rr}$$

## **Novel Developed Fully Automated Test Bench**

- **Fully automated test bench** (LabVIEW and ETI DSP-System based)
- Max. DC-Link voltage:  $V_{\text{max}} = 1$ , 8 kV
- Maximal current pulse:  $I_{max} = 4 \text{ kA}$
- Maximal measurable current slope:  $di/dt = 6\frac{\kappa A}{ma}$
- Adjustable junction temperature:  $-20 \text{ °C} < T_{I} < +160 \text{ °C}$
- Measurement of conduction characteristics





Measurement of switching behavior

Evaluation of the measured data with Matlab and LabVIEW

- Fully automated switchable choke
  - Configurable choke with seven different effective inductances
  - Selection of desired inductivity via thyristors

DUT

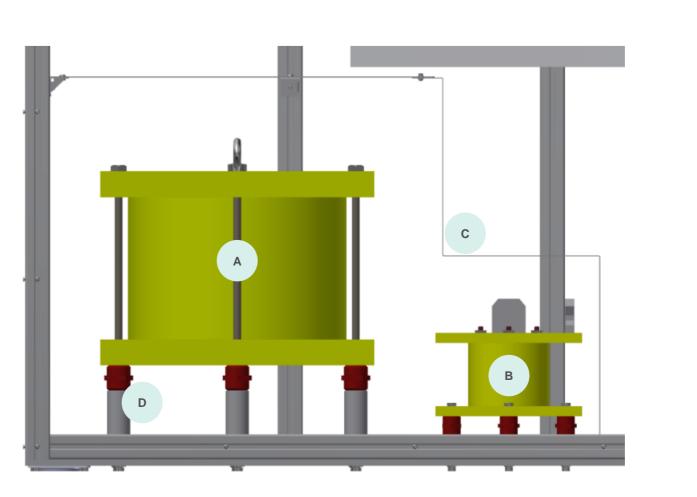
voltage

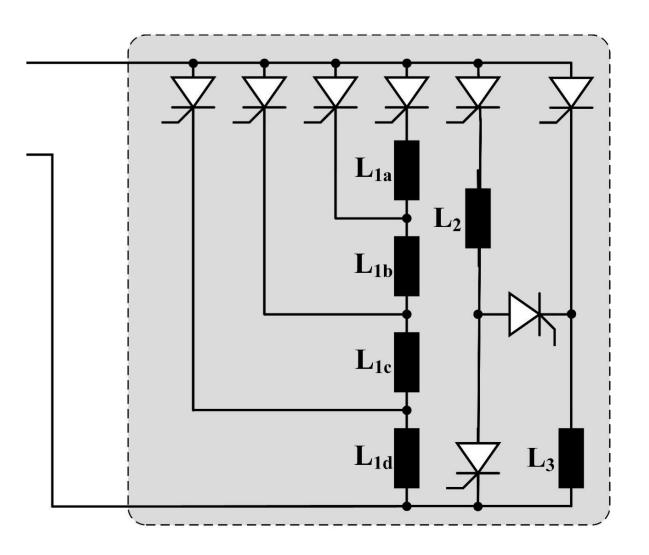
displays

temperature

display

ensuring safe operation





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